

REMARKS

Claims 1-44 are pending in the application, with claims 1-3 and 19 being independent. Claims 1-3, 12, 18-21, 23-26, and 29-30 have been amended, and claims 31-44 have been added.

Support for the amendments to claims 1-3 to recite either that the source signal line driver circuit comprising a constant current source is electrically connected to one of the plurality of source signal lines, or that each of the plurality of source signal line driver circuits comprises a constant current source that is electrically connected to at least one of the plurality of signal lines may be found on page 14, lines 6-30, and Figs. 7 and 8. Support for the amendment to claims 1-3 to recite a switching element and a light-emitting element may be found on page 6, lines 13-16.

Support for the amendment to claim 12 to recite that the gate signal line driver circuit is formed over the same substrate as the switching element may be found on page 7, lines 23-24. Support for the amendment to claim 19 to recite supplying current from a constant current source in a source signal line driver circuit to one of a plurality of source signal lines may be found on page 6, lines 13-20.

Support for the amendments to claims 20, 23, and 24 to recite either that each of the switching elements comprises one thin film transistor or that the switching element comprises one thin film transistor may be found on page 7, lines 9-10. Support for the amendments to claims 21, 25, and 26 to recite either that each of the switching elements comprises a multi-gate thin film transistor or that the switching element comprises a multi-gate thin film transistor may be found on page 7, lines 10-11.

Support for new claims 31-36 and 38-43 may be found on page 7, lines 3-9. Support for new claims 37 and 44 may be found on page 7, lines 29-30.

No new matter has been introduced.

Anticipation Rejection

Claims 1-30 stand rejected under 35 U.S.C. 102(e) as being anticipated by Kazutaka (JP 2004-077567). Applicant requests reconsideration and withdrawal of this rejection because Kazutaka does not describe or suggest either a source signal line driver circuit comprising a

constant current source electrically connected to one of the source signal lines, as required by independent claims 1-3, or supplying current from a constant current source in a source signal line driver circuit to source signal line, as recited in independent claim 19.

Kazutaka is directed to obviating insufficient write times in high resolution display devices. See translation, paragraphs 5 and 6. In order to increase available write time, Kazutaka features X data lines (where 'X' represents a natural number of 4 or greater) that are used to supply video signals to X pixels simultaneously. See translation, paragraph 8. In Kazutaka, element 312 of Fig. 10A is a "control signal" that is "supplied from the control line." Further, element 312 is arranged in a pixel "to which a predetermined current is supplied from this current source 312 at a light emitting device 308." See translation, paragraph 43. Element 312 in Kazutaka is not a constant current source connected to a source signal line, and consequently, does not describe or suggest "a source signal line driver circuit comprising a constant current source where the constant current source is electrically connected to one of the plurality of source signal lines." Similarly, element 312 does not describe or suggest "supplying current from a constant current source signal line driver circuit to one of a plurality of source signal lines," as recited in claim 19.

For at least these reasons, the rejection of independent claims 1-3 and 19, and the accompanying dependent claims 4-18 and 20-30, should be withdrawn.


Applicant submits that all claims are in condition for allowance.

The fee in the amount of \$700 in payment for the excess claim fees is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: _____

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